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<p>(54) Title: NANOPARTICLE-BASED ELECTRICAL, CHEMICAL, AND MECHANICAL STRUCTURES AND METHODS OF MAKING SAME</p> <p>(57) Abstract</p> <p>Nanoparticles are utilized to create, through deposition and patterning, functional electronic, electromechanical, and mechanical systems. At sizes ranging from 1 to 999 nm, the ratio of surface atoms to interior atoms becomes non-negligible, and particle properties therefore lie between those of the bulk and atomic materials. Monodisperse (i.e., uniformly sized) or polydisperse nanoparticles can form stable colloids or suspensions in appropriate dispersing media, facilitating their deposition and processing in a liquid state. As a result, printing technology can be utilized to deposit and pattern nanoparticles for mass production or for personal desktop manufacturing.</p>			

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NANOPARTICLE-BASED ELECTRICAL, CHEMICAL, AND MECHANICAL STRUCTURES AND METHODS OF MAKING SAME

5

PRIOR APPLICATION

This application stems from U.S. Serial No. 60/097,037, filed on
August 19, 1998.

FIELD OF THE INVENTION

The present invention relates to fabrication of electronic, chemical,
10 and mechanical devices by deposition techniques such as printing.

BACKGROUND OF THE INVENTION

Electronic and electromechanical components are presently
fabricated in large, immobile manufacturing facilities that are tremendously
expensive to build and operate. For example, semiconductor device
15 fabrication generally requires specialized microlithography and chemical
etching equipment, as well as extensive measures to avoid process
contamination.

The large up-front investment required for manufacturing capacity
not only limits its general availability, but also increases the cost of
20 custom fabrication. For a small custom order to be financially competitive

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with mass production of an established device, the per-unit cost will necessarily be quite high— often out of reach for many designers.

Currently, the economics of device fabrication disfavors sophistication and small batch sizes.

5 In addition to their expense, the fabrication processes ordinarily employed to create electronic and electromechanical components involve harsh conditions such as high temperatures and/or caustic chemicals, limiting the ability to integrate their manufacture with that of functionally related but environmentally sensitive elements. For example, the high
10 temperatures used in silicon processing may prevent three-dimensional fabrication and large-area fabrication; these temperatures are also incompatible with heat-sensitive materials such as organic and biological molecules. High temperatures also preclude fabrication on substrates such as conventional flexible plastics, which offer widespread availability
15 and low cost.

 These fabrication processes are also subtractive, depositing a desired material over an entire substrate before removing it from undesired locations through techniques such as etching and lift-off. Subtractive processes are wasteful; introduce dangerous, costly, and environmentally
20 unfriendly chemicals to the fabrication process; and limit the range of

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manufacturable devices since the etch chemistry can interact with previously deposited layers.

Approaches toward reducing the cost of custom manufacture are described in copending application Serial No. 08/958,098 and published
5 PCT Application WO 98/03896. In accordance with these publications, semiconductor devices are fabricated by successive deposition of electrically active layers in a manner analogous to conventional printing. The described processes facilitate manufacture outside of vacuum, in an arbitrary pattern and on a wide range of substrates, without the need for
10 specialized techniques such as chemical etching. They are also additive, confining material deposition to appropriate areas.

These processes, while highly advantageous in some applications, are nonetheless limited in terms of the properties achievable by the resulting devices. In particular, devices based on electrically active
15 particles dispersed in an inert binder tend to be limited in terms of clock speed owing to the permanent spacing between particles. While it is possible to increase clock speed by fusing the particles into a continuous layer, this requires high-temperature processing which is costly and, as noted above, may damage the substrate or other electrically active layers.

DESCRIPTION OF THE INVENTION

Brief Summary of the Invention

The present invention overcomes the limitations of the prior art through the innovative use of nanoparticles to create, through deposition
5 and patterning, functional electronic, electromechanical, chemical, and mechanical systems. The invention exploits the fact that many physical, electrical, and optical properties that appear constant in the bulk of organic and inorganic materials are size-dependent at the very small scales characteristic of nanoparticles. At these sizes—ranging from nearly 1 to
10 999 nm—the ratio of surface atoms to interior atoms becomes non-negligible, and particle properties therefore lie between those of the bulk and atomic materials. Monodisperse (i.e., uniformly sized) or polydisperse nanoparticles can form stable colloids in appropriate dispersing media, facilitating their deposition and processing in a liquid state. As a result,
15 additive printing technology can be utilized to deposit and pattern nanoparticles for mass production or for personal desktop manufacturing.

Furthermore, a key property that changes at small sizes is melting point. The effect is substantial; in some semiconductors, melting points have been observed to drop more than 1000 °C from the bulk material.
20 The melting point depression observed in nanoparticle systems facilitates

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the low-temperature sintering, annealing, and melting of nanoparticles into crystalline films. As a result, nanoparticles can be printed and heated at low temperatures to form films of the bulk material, or can instead be printed and left in dispersed form to retain the size-dependent properties
5 characteristic of the nanoparticles.

Unlike conventional electrically active particles, nanoparticles are formed not by grinding, but instead via chemical methods (such as pyrolysis) or physical methods (such as physical vapor synthesis). Nanoparticles useful in accordance with the present invention may be
10 pristine or surrounded by a "capping" group to passivate the surface, alter surface chemistry, facilitate dispersion in a liquid, or modify some other aspect of the particle's morphology or behavior. Following their deposition, nanoparticles can self-assemble to form highly ordered thin films and superlattices that may exhibit multiple phases.

15 Accordingly, in a preferred embodiment, the invention comprises a method of fabricating an active component comprised of portions consisting of different materials, at least one of the starting materials being in the form of nanoparticles dispersed in a carrier. The nanoparticles differ from a bulk material of identical composition in at least one physical
20 property, thereby conferring a desired functionality or processing advantage. In accordance with the method, the materials are deposited

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onto a substrate in a succession of layers which, in sum, define a selected electrical, mechanical, chemical, and/or electromechanical activity of the component. The nanoparticles within each deposited layer are immobilized (by fusing, melting, or annealing the particles into a continuous material, by curing the carrier into a permanent matrix, by surrounding the
5 nanoparticles with bifunctional surface groups that link adjacent nanoparticles, or merely by evaporating the carrier using heat or low pressure) in order to facilitate performance of the intended function. Also within the scope of the invention are components fabricated in accordance
10 with the methods hereof.

Brief Description of the Drawings

The foregoing discussion will be understood more readily from the following detailed description of the invention, when taken in conjunction with the accompanying drawings, in which:

- 5 FIG. 1 is a greatly enlarged side elevation of a thin-film transistor device, showing its mode of fabrication in accordance with the present invention;
- FIG. 2 graphically illustrates the melting temperature vs. particle size of CdS nanoparticles;
- 10 FIG. 3 schematically illustrates an embodiment of the invention configured as a printer;
- FIG. 4 illustrates an electronic identification tag fabricated entirely by printing in accordance with the present invention;
- FIG. 5 schematically depicts a three-phase linear motor realized in
15 accordance with the present invention;
- FIG. 6 illustrates a microelectromechanical cantilever fabricated in accordance with the present invention; and

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FIG. 7 illustrates a chemFET fabricated in accordance with the present invention.

Detailed Description of the Preferred Embodiments

5 1. Nanoparticle Synthesis and Suspension

Particles useful in accordance with the present may be monodisperse or polydisperse, and may represent a homogeneous suspension of a single species of nanoparticle or a heterogeneous suspension of different nanoparticles. Representative classes of
10 nanoparticle include insulators (e.g., silicon dioxide); semiconductors (e.g., silicon or cadmium selenide); and conductors (e.g., silver).

Numerous synthetic approaches, including pyrolysis of organometallic precursors, arrested precipitation, precipitation in reverse micelles, and exchange (metathesis) reactions, have been used to
15 generate nanoparticles. In principle, virtually any nanoparticle synthesis is appropriate for the production of nanoparticles for the purposes of the present invention. However, because nanoparticle properties depend strongly on size, shape, crystallinity, and surface derivatization, in practice the particle synthesis is tailored so as to control these parameters for a

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particular application. In general, if the nanoparticles are not intended for bulk thick or thin film deposition— i.e., they will be applied and utilized in their native particulate state without subsequent fusion into the bulk material— then any synthesis yielding appropriate size control and

5 crystallinity can be utilized.

On the other hand, if the particles are printed as a precursor to bulk thick or thin films, the synthesis should yield particles without tightly bound or heavy capping groups. Capping groups (sometimes used during synthesis to retard particle growth) can be problematic in bulk film

10 applications if they become trapped as impurities or impede crystal growth, thereby compromising film quality. Pyrolytic syntheses using pyridines and other coordinating solvents (*see, e.g., Murray et al., J. Am. Chem. Soc.* **115**:8706 (1993)), micelle-based approaches (*see, e.g., Steigerwald et al., J. Am. Chem. Soc.* **110**:3046 (1988)), and some

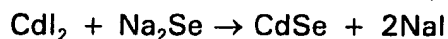
15 metathetic preparations (*see, e.g., Jarvis et al., Mat. Res. Soc. Symp. Proc.* **272**:229 (1992)) can all yield particles free of heavy organic capping groups.

In synthesizing Group II-VI semiconductor nanoparticles, in particular CdSe, we have further developed the metathesis synthesis to

20 form very small particles without the use of heavy capping groups. Such small sizes are desirable for their low melting point, as discussed in further

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detail below. In an oxygen- and water-free environment such as a nitrogen atmosphere maintained in a glove box, equimolar amounts of methanol-based solutions of CdI_2 and Na_2Se are combined in a solvent, such as pyridine, that both interacts with the particles and retards their
5 growth. The particles immediately begin to reversibly flocculate and fall from solution, forming CdSe according to the equation



After filtering or decanting, the particles are washed with methanol to remove the soluble byproduct, NaI, and the particles are then dispersed
10 in pyridine. In principle, other coordinating solvents such as amines, pyridines, and furans will work as well. In pyridine, it is found that the particles ripen and the size distribution narrows as the particles form the most thermodynamically stable size species. For CdSe in pyridine, these species are all below 2 nm in diameter. At such a small size the particles
15 are believed to be analagous in size and shape to tetrahedral clusters reported in Herron et al., *Science* 259:1426 (1993). Because of the molecular character and closed structural shell, particles with this structure are strongly favored thermodynamically, thus controlling size and keeping the particles below 2 nm in diameter.

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By choosing appropriate reagents and a coordinating solvent, it should be possible to generalize this approach to form a number of weakly capped nanoparticle species at sizes appropriate for low-temperature thermal processing into bulk crystalline films. For the CdSe particles
5 described, the volatile solvent pyridine acts as the capping group. The critical feature of this synthesis is the coordinating solvent. It can function as both a reaction solvent to which the reagents are added, or as a component of a reagent solution. Either method of introducing the pyridine works well for limiting particle growth to sizes below 2 nm, and
10 pyridine is readily removed from the nanoparticle surface upon heating.

Nanoparticles may be passivated at the surface by an organic capping group, which surrounds the typically inorganic particle core as a shell. In such circumstances, it is the capping group that largely determines the solubility of the particles, and thus what solvents are
15 appropriate for carrying the particles. Additionally, the capping groups can act as a critical component in a device. For example, the caps can act as electrical insulators or sensitizers. An alkylthiol (such as 1,10-decanedithiol) is useful as an insulator and also to cross-link adjacent particles, while semiconductor nanoparticles can be surrounded by a
20 semiconductor polymer of different electron affinity, which thereby serves as a sensitizer. For example, photovoltaic devices have been formed by

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capping CdSe nanoparticles with poly(2-methoxy,5-(2'-ethyl-hexyloxy)-*p*-phenylenevinylene) (see Alivisatos, *MRS Bulletin*, Feb. 1998, at 18). If desired, the capping group may be removed following deposition by application of heat and/or vacuum.

5 Inorganic materials such as ZnS can also cap particles. These may exhibit better adhesion to inorganic nanoparticles than organic molecules, and may improve properties such as luminescence. An inorganic shell also offers a way of introducing mixed materials into devices via a single type of particle, e.g., CdSe and CdS in a CdSe/CdS core/shell semiconductor
10 nanoparticle (synthesis of which is described in Peng et al., *J. Am. Chem. Soc.* **119**:7019 (1997)). More typically, however, an organic cap will be applied to the inorganic particle in order to confer a property of interest. Inorganic shells are formed during synthesis by simply growing an outer layer of the shell material on the particle. This may be accomplished by
15 introducing the shell reagents to the core nanoparticle suspension under appropriate reaction conditions.

Nanoparticles are known to self-assemble into ordered arrays and superlattices, both with and without capping groups. If such ordered films orient not only particles but also their crystal planes, then long-range
20 crystal order may be expected upon sintering, annealing, or melting. Highly faceted particles may be more likely to align their crystal planes

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than unfaceted particles. Self-assembly also offers the possibility of printing multiple phases or layers in a single pass, or for printing some pre-assembled structures or structures that assemble during the printing. Thus, different layers or regions might form as a result of a single printing
5 step. Layers may form by size affinity, with large and small particles tending to group with other particles of similar size. *See, e.g.,* Heath et al., *J. Phys. Chem. B* 101:189 (1997) and Dabbousi et al., *Chem. of Mat'ls.* 6:216 (1994), showing size-based particle clustering. Three-dimensional structures have been observed to occur when particles of
10 different sizes are brought together and small particles cluster around single large ones.

2. Device Fabrication

Nanoparticles suspended in a liquid can be deposited and patterned onto a substrate using any of a wide variety of processes, including ink
15 jetting, spin-coating, casting, lithography, gravure printing, screen printing, impact printing, stamping, contact printing (whereby a liquid or solid pattern is transferred from a plate, stamp or cylinder), or transfer onto the substrate through a mask. Moreover, nanoparticles may be applied in the dry state by dry jetting, laser printing or electrophotography (whereby a
20 substrate receives a charge pattern and the particles are drawn to the charged regions). Deposition or application may generally occur in an

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uncontrolled atmospheric environment, but a controlled atmosphere generally provides better process control. The deposited layers may serve as conductive, semiconductive, or insulating layers, chemically active layers, or as etch resists, light barriers, diffusion barriers, passivation
5 layers, adhesion layers, encapsulants, or structural supports.

A representative application of the present invention is fabrication of thin-film transistors (TFTs). TFTs have heretofore been formed from, for example, CdSe by evaporation or sputtering. An attractive feature of CdSe is that in the bulk it can be annealed at a relatively low temperature
10 (1-4 hours at 350 °C) in a dry air or nitrogen atmosphere. Annealing of the bulk film improves device performance by reducing defects as well as the number of donors, which tend to result from Se vacancies in the deposited film.

A CdSe TFT may be fabricated in accordance with the present
15 invention as shown in FIG. 1. One or more deposition devices 100, movable in two (or three) dimensions, is scanned over a substrate and selectively activated to apply onto a substrate 110 a series of layers that collectively define the TFT. In particular, the TFT comprises a conductive gate layer 115, an insulator layer 120, a semiconductor layer 125, and
20 source and drain conducting elements 130s, 130d. In the absence of an electrical gate 115, the structure in FIG. 1 can act as a light-sensitive

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photo-transistor. Deposition device 100 may be an ink-jet system configured to deliver a selected one of a series of solutions, colloids, and/or dispersions of one or more materials (as described, e.g., in the '098 application mentioned above, the entire disclosure of which is hereby
5 incorporated by reference), or may instead be an ink-jet system utilizing multiple jet heads, each ejecting a single material.

Thus, a nanoparticle silver, chromium, gold, copper, aluminum or other metallic dispersion may be used to form gate 115 by deposition onto substrate 110. To form layer 120, a nanoparticle dispersion of silicon
10 dioxide or barium titanate is deposited onto layer 115. The nanoparticle metal used for layer 115 may again be utilized to form source and drain elements 130s, 130d by deposition onto layer 120. And a CdSe (or other semiconductors, such as CdS, CdTe, or Si) nanoparticle dispersion is used to form layer 125, also by deposition onto layer 120. These materials
15 may be left as deposited, or may instead be sintered, annealed, or melted to form layers with electronic properties approaching those of the bulk material. Annealing, for example, can take place during printing, after each printing step, or following deposition. As with any bulk film, annealing can consist of multiple stages of treatment at different
20 temperatures (e.g., temperature ramp-up and/or thermal shock), heating mechanisms, durations, or ambients to improve selected electrical and/or

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mechanical properties. To anneal the particles, the temperature of the environment is elevated to a point high enough to remove the capping groups (if any) and to cause fusion, annealing, or melting of particles in all layers for which a bulk film is desired. Alternatively, electromagnetic
5 radiation, such as from a heat lamp or laser, may be used to thermally convert the nanoparticles to their bulk state.

The melting temperature of nanoparticles is highly dependent on particle size, and is desirably well below that of the bulk material. The melting behavior of CdS as a function of particle diameter is illustrated, for
10 a representative size range, in FIG. 2. All nanoparticles within the illustrated size range have melting points well below that of the bulk material. For particles below 2.5 nm in diameter, temperatures as low as 250 °C are adequate.

With reference to FIG. 3, an alternative fabrication embodiment is
15 configured as a printer 300 and includes rollers representatively indicated at 305, 310 for conducting the substrate 110 through a printing path. A series of deposition (e.g, ink-jet) heads representatively indicated at 315, 320 apply different nanoparticle-containing liquids onto appropriate portions of substrate 110 as it passes beneath them. Because substrate
20 110 is in motion along one dimension, heads 315, 320 need only translate along the orthogonal dimension to effect a complete scan of the substrate

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surface. A heating element 330, which emits heat or electromagnetic radiation, sinters, melts, cures or anneals the deposited material.

(Alternatively, the substrate may be heated during deposition—e.g., by hot stamping—to cause annealing.) The printer 300 may be

5 connected to an external source of data, such as the Internet, which encodes the desired device and thereby determines the pattern and materials that will be applied to substrate 110 by heads 315, 320. As a result, electronic hardware may be purchased over the Internet; the printer's owner downloads the appropriate file, which is executed on
10 printer 300 to facilitate near instantaneous delivery of the purchased equipment.

Without heating, the nanoparticles retain their size-dependent properties, and in a transistor structure an unannealed nanoparticle semiconductor can form a single-electron transistor.

15 The transistor illustrated in FIG. 1 may be connected to other printed or non-printed components to form functional electronic circuits. The means of connection may, for example, be established by deposition of nanoparticle conductors and, if necessary, insulators. These circuits may be connected to other printed or non-printed electronic, mechanical,
20 chemical, or electromechanical devices. For example, with reference to FIG. 4, an electronic identification tag 400 is fabricated on a dielectric

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substrate 410 entirely by printing. Nanoparticle conductors are printed onto a surface 415 of substrate 410, forming a spiral structure 420 that behaves as an inductor. The inner terminus 425 of spiral structure 420 is configured as a pad that acts as one plate of a capacitor. The outer

5 terminus of spiral structure 420 penetrates the substrate 410 and connects to a second pad 430 directly underlying pad 425. Pads 425, 430 together form a capacitor, and are connected to a logic module 440. Module 440 comprises a series of thin film transistors that are also applied by printing (each conforming, for example, to the device shown in FIG. 1)

10 and are interconnected to perform arbitrary logic functions. When the device 400 is excited by the magnetic field created by an external coil, preferably at the resonance frequency established by the capacitor and inductor, the logic module 440 modulates the device's response so as to convey a series of bits— i.e., the device's signature— in the time domain

15 to the external coil. This allows electronics connected to the external coil to read the device's signature.

Nanoparticle layers may also be used for their structural qualities in the fabrication of mechanical and electromechanical devices. Such devices include, for example, the linear and rotary actuators described in

20 the '098 application, as well as devices such as the three-phase linear motor illustrated in FIG. 5 and the conductive cantilever structure

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illustrated in FIG. 6. With reference to FIG. 5, a three-phase linear drive motor 500 consists of three patterned electrical conductors 510, 511, and 512 driven by a logic module 520. Conductors 510, 511, and 512 are fabricated and patterned using nanoparticle colloids applied to a substrate (e.g., plastic) as described above. Logic module 520 comprises a collection of thin film transistors interconnected so as to drive conductors 510, 511, and 512 with a three-phase voltage source (not shown). This three-phase voltage causes the motion of polarizable (high dielectric constant, low dielectric loss) particles 530 that are placed on the surface of conductors 510, 511, and 512.

FIGS. 6A and 6B illustrate construction of a conductive cantilever 600 using the approach of the present invention. With reference to FIG. 6A, first and second nanoparticle layers 602, 604 are sequentially deposited onto a suitable (e.g., plastic) substrate 606. A pair of regions 608, 610 within the nanoparticle layers 602, 604 are selectively converted to conductive bulk material with improved structural properties by application of heat from a laser beam or resistively heated AFM (atomic force microscope) tip. The cantilever 600 is revealed by a release etching step, which removes the unconverted nanoparticle material.

Nanoparticle layers may also be used for their chemical functionality (i.e., their intrinsic chemical properties or those of the capping group).

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Such functionality can serve as an adhesion layer or etch resist, or as a site for chemical modification and detection. As an example, transistor structures similar to those described in FIG. 1 can be modified such that the conductive gate is replaced with a chemically sensitive material, such as single-stranded oligonucleotides (see Souteyrand et al., *J. Phys. Chem. B* 101:2980 (1997)), thereby forming a so-called "chemFET" device. This modification is achieved by specific chemical interaction with the surface of the nanoparticle or nanoparticle-derived film.

As shown in FIG. 7, a chemFET 700 comprises a semiconductor layer 710, source and drain elements 720d, 720s, and a dielectric layer 725 applied thereover. Tailoring the surface of the dielectric layer 725 facilitates the detection of specific chemicals and/or biological materials 730 disposed on layer 725 through interaction therewith. The ionic charge of such chemicals places an electric field across the device, and, similar to the electronic gate in the transistor structure of FIG. 1, modifies the conducting properties of the semiconductor channel. In the case of oligonucleotides, the hybridization of the oligonucleotide can be detected. For example, layer 725 may be an insulator film fabricated by sintering a deposited nanoparticle colloid and subsequently treating the material to expose reactive hydroxyl groups that bind with the chemical or biological material. Alternatively, a dielectric layer can consist of unsintered

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nanoparticles. These will act as an insulator due to retention of the capping group, which thereby provides sites for chemical modification (so that the biological material reacts with the cap, or a material subsequently bound to the cap, rather than with the inorganic nanoparticle material).

- 5 Although the present invention has been described with reference to specific details, it is not intended that such details should be regarded as limitations upon the scope of the invention, except as and to the extent that they are included in the accompanying claims.

What is claimed is:

CLAIMS

- 1 1. A method of fabricating an active component comprised of portions
2 consisting of different materials, at least one of the materials being in the
3 form of nanoparticles dispersed in a carrier, the nanoparticles having a
4 submicron size and differing from a bulk material of identical composition
5 in at least one physical property, the method comprising the steps of:
6 a. applying the materials onto a substrate in a succession of layers
7 which, in sum, define a selected activity of the component, the
8 activity including at least one of electrical activity, mechanical
9 activity, chemical activity, electromechanical activity,
10 electromagnetic activity and optical activity, each layer
11 representing a portion of the component; and
12 b. immobilizing the nanoparticles to form at least one uniform layer
13 thereof.
- 1 2. The method of claim 1 wherein the nanoparticles comprise an
2 inorganic core particle surrounded by organic capping groups.
- 1 3. The method of claim 1 wherein the nanoparticles comprise an
2 inorganic core particle surrounded by an inorganic shell.

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- 1 4. The method of claim 3 wherein the inorganic shell is itself
2 surrounded by an organic capping groups.
- 1 5. The method of claim 1 wherein the nanoparticles are immobilized by
2 curing the carrier into a solid matrix.
- 1 6. The method of claim 2 wherein the nanoparticles are immobilized by
2 cross-linking the organic capping groups.
- 1 7. The method of claim 1 wherein the nanoparticles are immobilized by
2 evaporation of the carrier through the application of heat or low pressure.
- 1 8. The method of claim 2 wherein the organic capping group is
2 removed by the application of at least one of heat and vacuum.
- 1 9. The method of claim 1 wherein at least one layer comprises
2 nanoparticles of at least one semiconductor.
- 1 10. The method of claim 1 wherein the deposited materials include at
2 least one layer of semiconductive nanoparticles, at least one layer of
3 conductive nanoparticles, and at least one layer of insulating
4 nanoparticles.
- 1 11. The method of claim 10 further comprising the step of causing the
2 nanoparticles to form continuous, discrete layers.
- 1 12. The method of claim 11 wherein the layers are formed by at least
2 one of sintering, melting, and annealing.

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1 13. The method of claim 1 wherein at least some of the layers are
2 applied in a liquid state by a deposition process.

1 14. The method of claim 13 wherein the deposition process is selected
2 from the group consisting of ink jetting, spin-coating, casting, lithography,
3 gravure printing, screen printing, impact printing, stamping, roll-to-roll
4 printing, and contact printing.

1 15. The method of claim 1 wherein at least some of the layers are
2 applied in a dry state.

1 16. The method of claim 15 wherein said layers are applied by at least
2 one process selected from the group consisting of dry jetting, laser
3 printing or electrophotography.

1 17. The method of claim 9 wherein the semiconductor comprises CdSe.

1 18. The method of claim 1 further comprising the steps of:

2 a. receiving, from a remote site, data specifying the active
3 component in terms of the different materials and their
4 placement; and

5 b. locally performing the applying step in accordance with the data.

1 19. The method of claim 17 wherein the data is received via a
2 computer network.

1 20. A component fabricated in accordance with claim 1.

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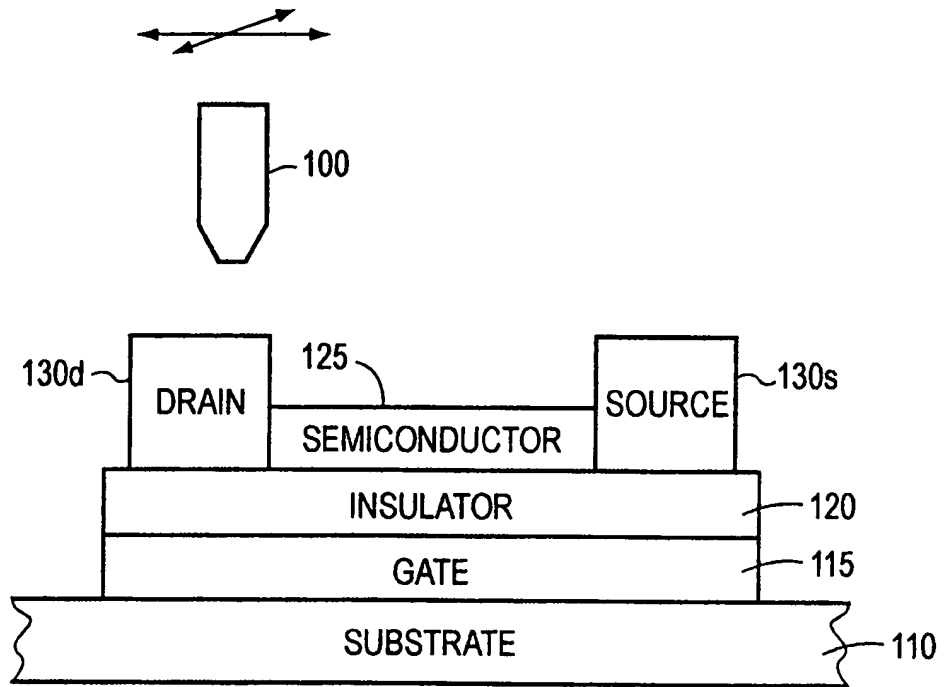


FIG. 1

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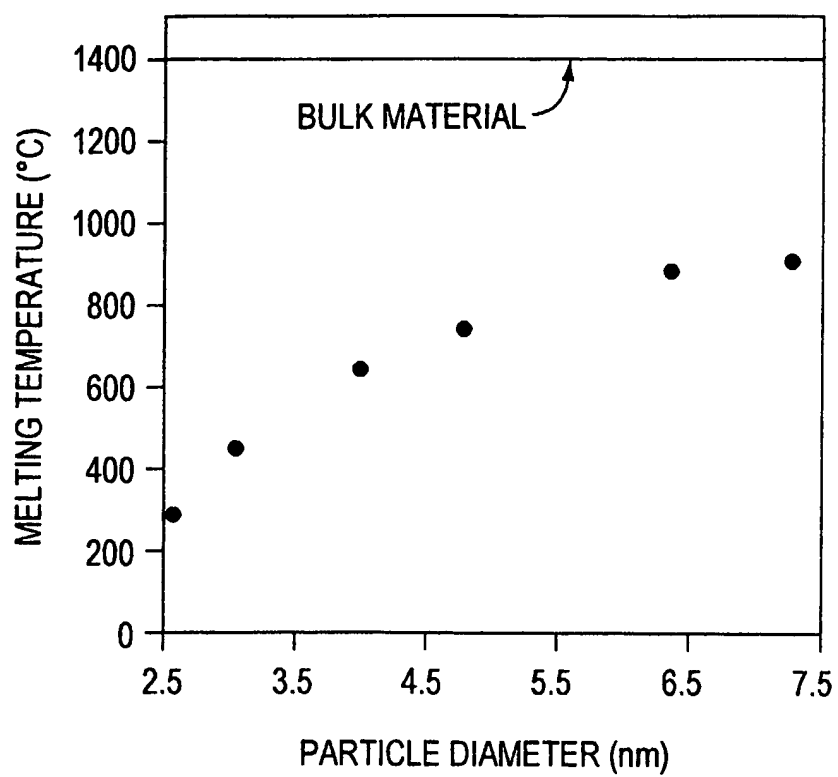


FIG. 2

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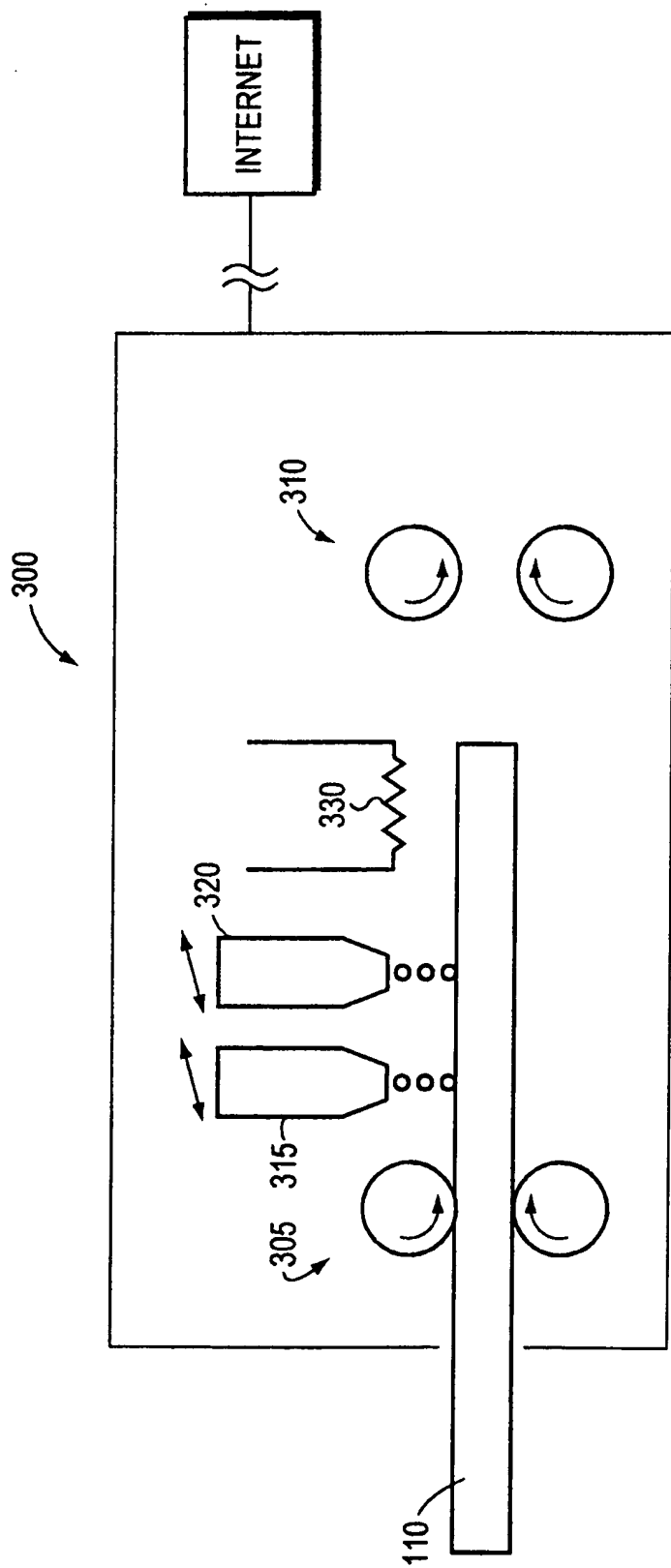


FIG. 3

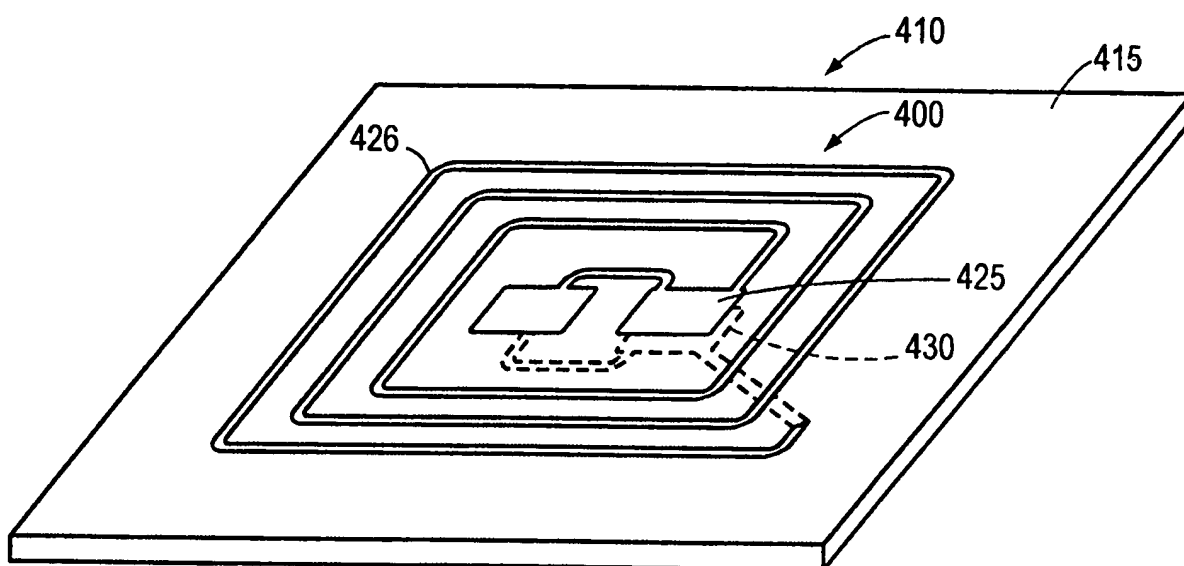


FIG. 4

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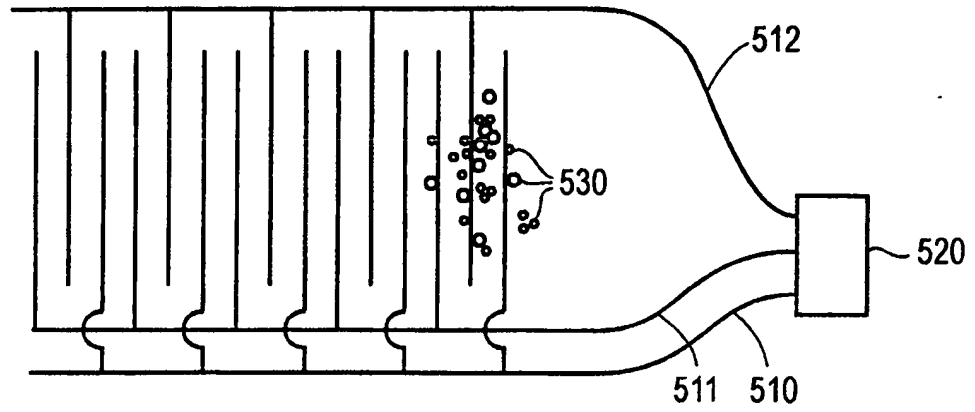


FIG. 5

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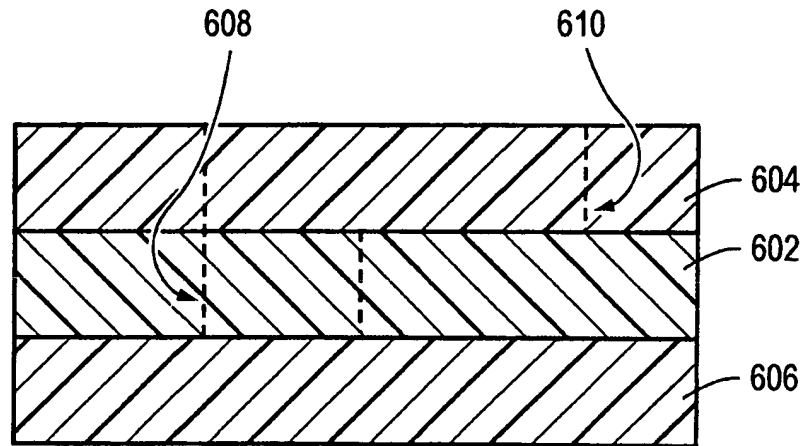


FIG. 6A

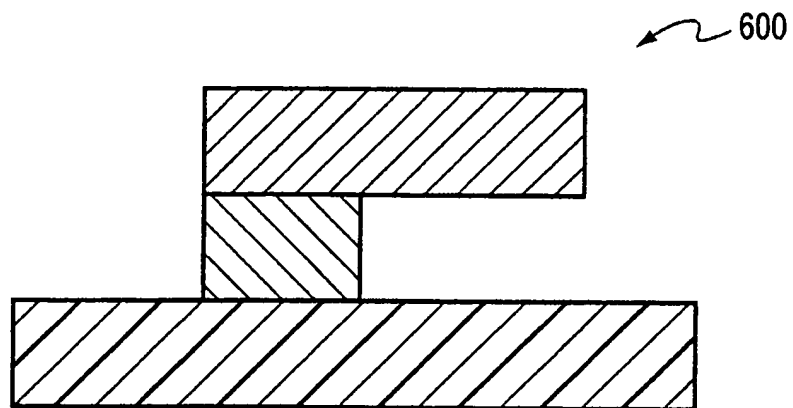


FIG. 6B

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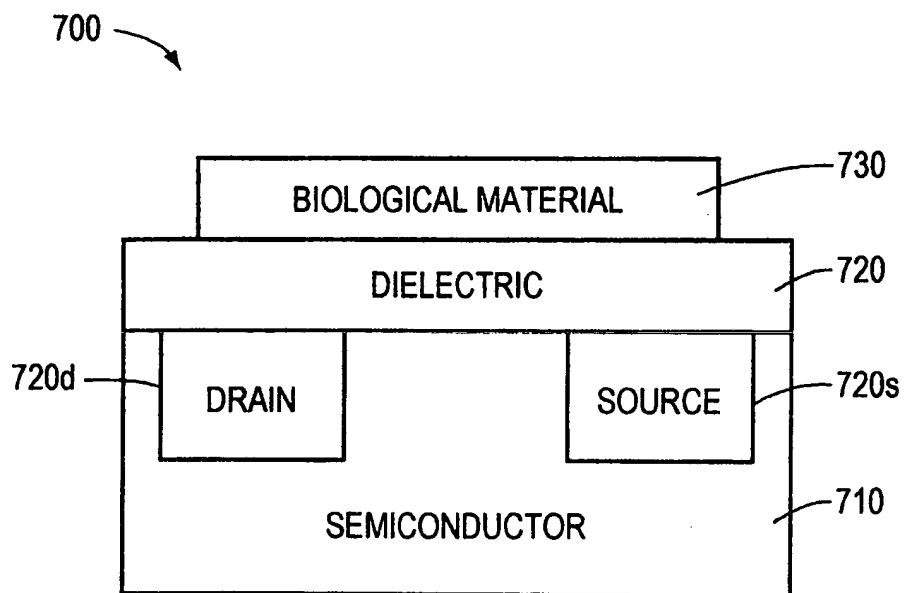


FIG. 7

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INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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(21) International Application Number: PCT/US99/18276 (22) International Filing Date: 11 August 1999 (11.08.99) (30) Priority Data: 60/097,037 19 August 1998 (19.08.98) US 09/334,873 17 June 1999 (17.06.99) US (71) Applicant: MASSACHUSETTS INSTITUTE OF TECHNOLOGY [US/US]; 77 Massachusetts Avenue, Cambridge, MA 02139 (US). (72) Inventors: JACOBSON, Joseph, M.; 223 Grant Avenue, Newton, MA 02159 (US). HUBERT, Brian, N.; Ashdown House, #608C, 305 Memorial Drive, Cambridge, MA 02139 (US). RIDLEY, Brent; Eastgate #20B, 60 Wadsworth Street, Cambridge, MA 02142 (US). NIVI, Babak; 253 Commonwealth Avenue, Boston, MA 02116 (US). FULLER, Sawyer; 1517 Nipomo Avenue, Los Osos, CA 93402 (US). (74) Agents: FRANK, Steven, J. et al.; Cesari and McKenna, LLP, 30 Rows Wharf, Boston, MA 02110 (US).			(81) Designated States: AL, AU, BA, BB, BG, BR, CA, CN, CU, CZ, EE, GD, GE, HR, HU, ID, IL, IN, IS, JP, KP, KR, LC, LK, LR, LT, LV, MG, MK, MN, MX, NO, NZ, PL, RO, SG, SI, SK, SL, TR, TT, UA, UZ, VN, YU, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i> (88) Date of publication of the international search report: 13 July 2000 (13.07.00)
(54) Title: NANOPARTICLE-BASED ELECTRICAL, CHEMICAL, AND MECHANICAL STRUCTURES AND METHODS OF MAKING SAME			
(57) Abstract Nanoparticles are utilized to create, through deposition and patterning, functional electronic, electromechanical, and mechanical systems. At sizes ranging from 1 to 999 nm, the ratio of surface atoms to interior atoms becomes non-negligible, and particle properties therefore lie between those of the bulk and atomic materials. Monodisperse (i.e., uniformly sized) or polydisperse nanoparticles can form stable colloids or suspensions in appropriate dispersing media, facilitating their deposition and processing in a liquid state. As a result, printing technology can be utilized to deposit and pattern nanoparticles for mass production or for personal desktop manufacturing.			

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INTERNATIONAL SEARCH REPORT

Inta Jonal Application No

PCT/US 99/18276

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 G02F1/01 C09K3/00 H01L21/20

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 C09K G02F H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 93 10564 A (UNIV CALIFORNIA) 27 May 1993 (1993-05-27) examples 14-16	1,2,7,9, 13,20
X	US 5 262 357 A (ALIVISATOS A PAUL ET AL) 16 November 1993 (1993-11-16) example 1	1,2,7,9, 13,20
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INTERNATIONAL SEARCH REPORT

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Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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INTERNATIONAL SEARCH REPORT

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PCT/US 99/18276

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Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Information on patent family members

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PCT/US 99/18276

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